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2811

Docket No.: P2000,0167 CIP

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By: _____ Date: April 22, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Herbert Palm et al.
Applic. No.	:	09/927,573
Filed	:	August 9, 2001
Title	:	Memory Cell, Memory Cell Configuration and Fabrication Method
Art Unit	:	2811

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

S i r :

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In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 3,731,163 (Shuskus), dated May 1, 1973;

United States Patent No. 5,966,603 (Eitan), dated October 12, 1999;

United States Patent No. 5,973,358 (Kishi), dated October 26, 1999;

German Published, Non-Prosecuted Patent Application DE 29 46 864 A1 (Bate et al.), dated June 4, 1980, non-volatile semiconductor memory components and method for producing them;

European Patent Application EP 0 967 654 A1 (Bohumil), dated December 29, 1999;

Japanese Patent Abstract JP 04 012 573 (Hirano), dated January 17, 1992;

International Search Report dated February 14, 2002.

In accordance with 37 C.F.R. 1.97(e) the undersigned herewith states that each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

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For Applicants

Date: April 22, 2002

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